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Vishay General Semiconductor

# Surface Mount Trench MOS Barrier Schottky Rectifier



DO-221AC

PRIMARY CHARACTERISTICS			
Package	DO-221AC (SlimSMA)		
I <sub>F(AV)</sub>	5.0 A		
V <sub>RRM</sub>	60 V		
I <sub>FSM</sub>	100 A		
$V_F$ at $I_F = 5.0$ A	0.48 V		
T <sub>J</sub> max.	150 °C		
Diode variations	Single die		

### FEATURES

- Very low profile typical height of 0.95 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

## MECHANICAL DATA

Case: DO-221AC (SlimSMA) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified Base P/NHM3\_X - halogen-free, RoHS-compliant, and AEC-Q101 qualified ("\_X" denotes revision code e.g. A, B,.....) Terminals: matte tin plated leads, solderable per J-STD-002 and JESD22-B102 M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

Polarity: color band denotes cathode end

meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	VSSAF56	UNIT	
Device marking code		V56		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	5.0	— A	
	I <sub>F</sub> <sup>(2)</sup>	2.6		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	100	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

#### Notes

(1) Mounted on 30 mm x 30 mm pad areas, 2 oz. FR4 PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

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COMPLIANT

HALOGEN

FREE

VSSAF56



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 2.5 A	Tr = 25 °C		0.47	-	V
	$I_{F} = 5.0 \text{ A}$			0.54	0.62	
	I <sub>F</sub> = 2.5 A	– T <sub>A</sub> = 125 °C		0.38	-	
	I <sub>F</sub> = 5.0 A			0.48	0.56	
Reverse current	V - 60 V	$V_{R} = 60 V = \frac{T_{A} = 25 \degree C}{T_{A} = 125 \degree C}$	I <sub>R</sub> <sup>(2)</sup>	-	0.4	mA
	$v_{\rm R} = 00 v$			4.5	15	
Typical junction capacitance	4.0 V, 1 MH	4.0 V, 1 MHz		540	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise specified)				
PARAMETER	SYMBOL	VSSAF56	UNIT	
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	115	°C/W	
	R <sub>0JM</sub> <sup>(2)</sup>	12	C/W	

#### Notes

 $^{(1)}$  Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

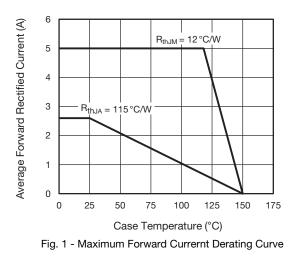
 $^{(2)}$  Mounted on 30 mm x 30 mm pad areas, 2 oz. FR4 PCB;  $R_{\theta JM}$  - junction to mount

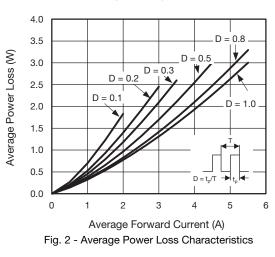
ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
VSSAF56-M3/6A	0.032	6A	3500	7" diameter plastic tape and reel		
VSSAF56-M3/6B	0.032	6B	14 000	13" diameter plastic tape and reel		
VSSAF56HM3/6A <sup>(1)</sup>	0.032	6A	3500	7" diameter plastic tape and reel		
VSSAF56HM3/6B <sup>(1)</sup>	0.032	6B	14 000	13" diameter plastic tape and reel		
VSSAF56HM3_A/H <sup>(1)</sup>	0.032	Н	3500	7" diameter plastic tape and reel		
VSSAF56HM3_A/I <sup>(1)</sup>	0.032	l	14 000	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified

### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise specified)





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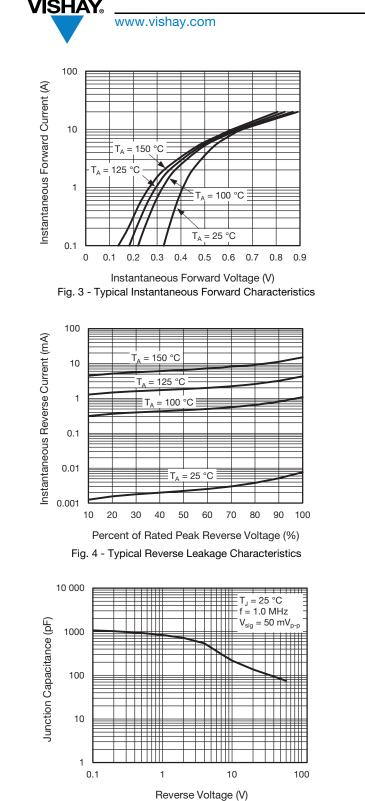
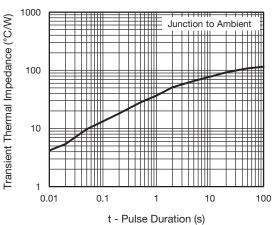
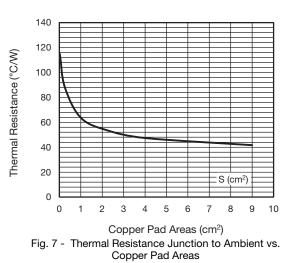


Fig. 5 - Typical Junction Capacitance







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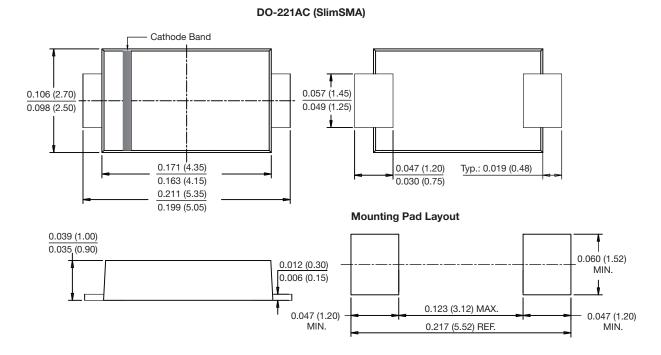
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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